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APPLICATION NO). F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/066,150	10/066,150 10/26/2001		Hung T. Nguyen	01-621	3566
24319	7590	12/10/2004		EXAMINER	
	IC CORPO	PRATION	MEONSKE, TONIA L		
MS: D-106		_		ART UNIT	PAPER NUMBER
MILPITAS	S, CA 950	35		2183	
				DATE MAILED: 12/10/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
0.00		10/066,150	NGUYEN, HUNG T.				
Office Ad	ction Summary	Examiner	Art Unit				
		Tonia L Meonske	2183				
The MAILING Period for Reply	DATE of this communication app	ears on the cover sheet with the c	orrespondence address				
THE MAILING DATE - Extensions of time may be after SIX (6) MONTHS fro - If the period for reply spec - If NO period for reply within the second proper in	E OF THIS COMMUNICATION. available under the provisions of 37 CFR 1.13 in the mailing date of this communication. ified above is less than thirty (30) days, a reply ecified above, the maximum statutory period v set or extended period for reply will, by statute,	Y IS SET TO EXPIRE 3 MONTH(36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE and the description of the	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status							
1) Responsive to	communication(s) filed on 27 Se	eptember 2004.					
2a)⊠ This action is I	∑ This action is FINAL. 2b) This action is non-final.						
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4a) Of the above 5) ☐ Claim(s) 6) ☒ Claim(s) <u>1-20</u> 7) ☐ Claim(s)		vn from consideration.					
Application Papers							
10)⊠ The drawing(s) Applicant may n Replacement dr	ot request that any objection to the of awing sheet(s) including the correction	r. a) □ accepted or b) ☑ objected drawing(s) be held in abeyance. See on is required if the drawing(s) is obj aminer. Note the attached Office	e 37 CFR 1.85(a). sected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C	. § 119						
a) All b) So 1. Certified 2. Certified 3. Copies of applications.	ome * c) None of: copies of the priority documents copies of the priority documents of the certified copies of the prior on from the International Bureau	s have been received in Application ity documents have been received	on No ed in this National Stage				
Attachment(s)							
1) Notice of References Cit	red (PTO-892)	4) Interview Summary					
	Patent Drawing Review (PTO-948) statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	ite atent Application (PTO-152)				

DETAILED ACTION

Drawings

1. This application lacks formal drawings. The informal drawings filed in this application are acceptable for examination purposes. When the application is allowed, applicant will be required to submit new formal drawings.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1, 3, 6, 7, 8, 10, 13, 14, 15, 17, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Leung et al, US Patent 5,784,603, in view of Arizono US Patent 4,910,664.
- 4. Claims 2, 9, and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Leung et al, US Patent 5,784,603, in view of Arizono US Patent 4,910,664, and Bogin et al., US Patent 5,835,435.
- 5. Claims 4, 11, and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Leung et al, US Patent 5,784,603, in view of Arizono US Patent 4,910,664, and Chi, US Patent 6,243,807.
- 6. Claims 5, 12, and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Leung et al, US Patent 5,784,603, in view of Arizono US Patent 4,910,664, and Tokuume, US Patent 4,965,722.

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7. The rejections are respectfully maintained and incorporated by reference as set forth in the last office action, mailed on June 28, 2004.

Response to Arguments

- 8. Applicant's arguments filed September 27, 2004 have been fully considered but they are not persuasive.
- 9. On pages 10 and 11, Applicant argues in essence:

"The Examiner then asserts that Arizono teaches the above-mentioned missing elements of Claim 1. However, a detailed reading reveals that Arizono does not teach this concept. Arizono instead teaches a processor architecture designed to efficiently implement a "while" loop... Nowehere in Arizono is there a teaching or suggestion of the element of "a loop recognizer...that determines whether a loop is present in fetched instructions and reinstates a validity of instructions in said loop and prevents said prefetch circuitry from prefetching instructions outside of said loop until said loop completes processing.""

However, Arizono has taught a loop recognizer...that determines whether a loop is present in fetched instructions (Column 3, Lines 10-67, column 5, lines 43-54, A while instruction is determined to be in the fetched instructions and the loop-beginning address register is loaded with the value from the program counter.) and reinstates a validity of instructions in said loop (Column 3, Lines 10-67, column 5, lines 43-54, Resetting the prefetch counter to the loop-beginning address reinstates the validity of the prior instructions in the loop.) and prevents said prefetch circuitry from prefetching instructions outside of said loop until said loop completes processing (Column 3, Lines 10-67, column 5, lines 43-54, The prefetch counter is reset to the loop-beginning address each time prefetch counter equals the loop-ending address, thereby preventing said prefetch circuitry from prefetching instructions outside of said loop until said loop completes processing.). Therefore this argument is moot.

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10. On page 11, Applicant argues in essence:

"Neither reference contains a suggestion that the validity of prefetched instructions can or should ever be reinstated."

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However, Arizono has in fact taught that the validity of prefetched instructions are reinstated. In Arizono, resetting the prefetch counter to the loop-beginning address reinstates the validity of the prior instructions in the loop that are stored in the loop buffer (Column 3, Lines 10-67, column 5, lines 43-54). Therefore this argument is moot.

Conclusion

- 11. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).
- 12. A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.
- 13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tonia L Meonske whose telephone number is (571) 272-4170. The examiner can normally be reached on Monday-Friday, 8-4:30.

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14. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie P Chan can be reached on (571) 272-4162. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

15. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

tlm

EDDIE CHAN SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100